

FIG. 1

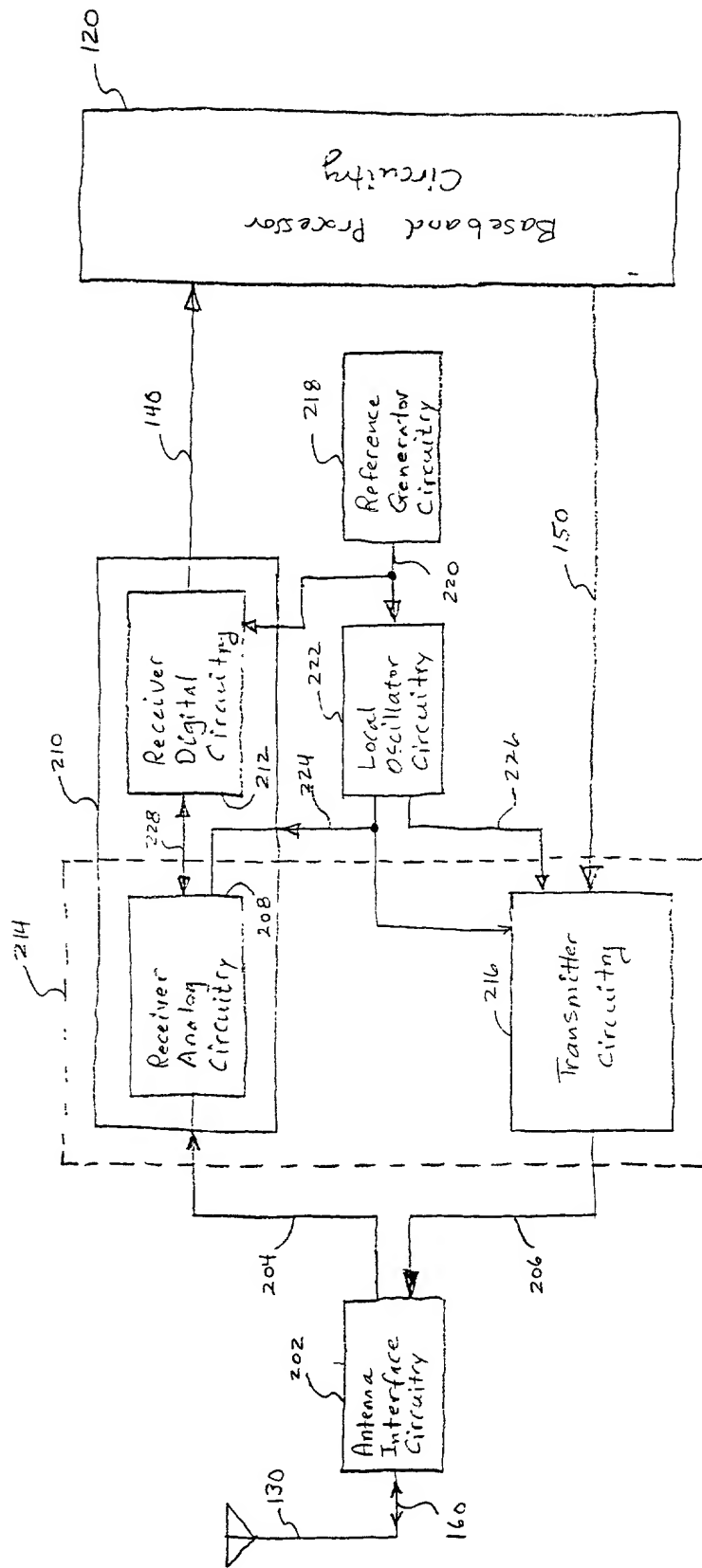


FIG. 2A

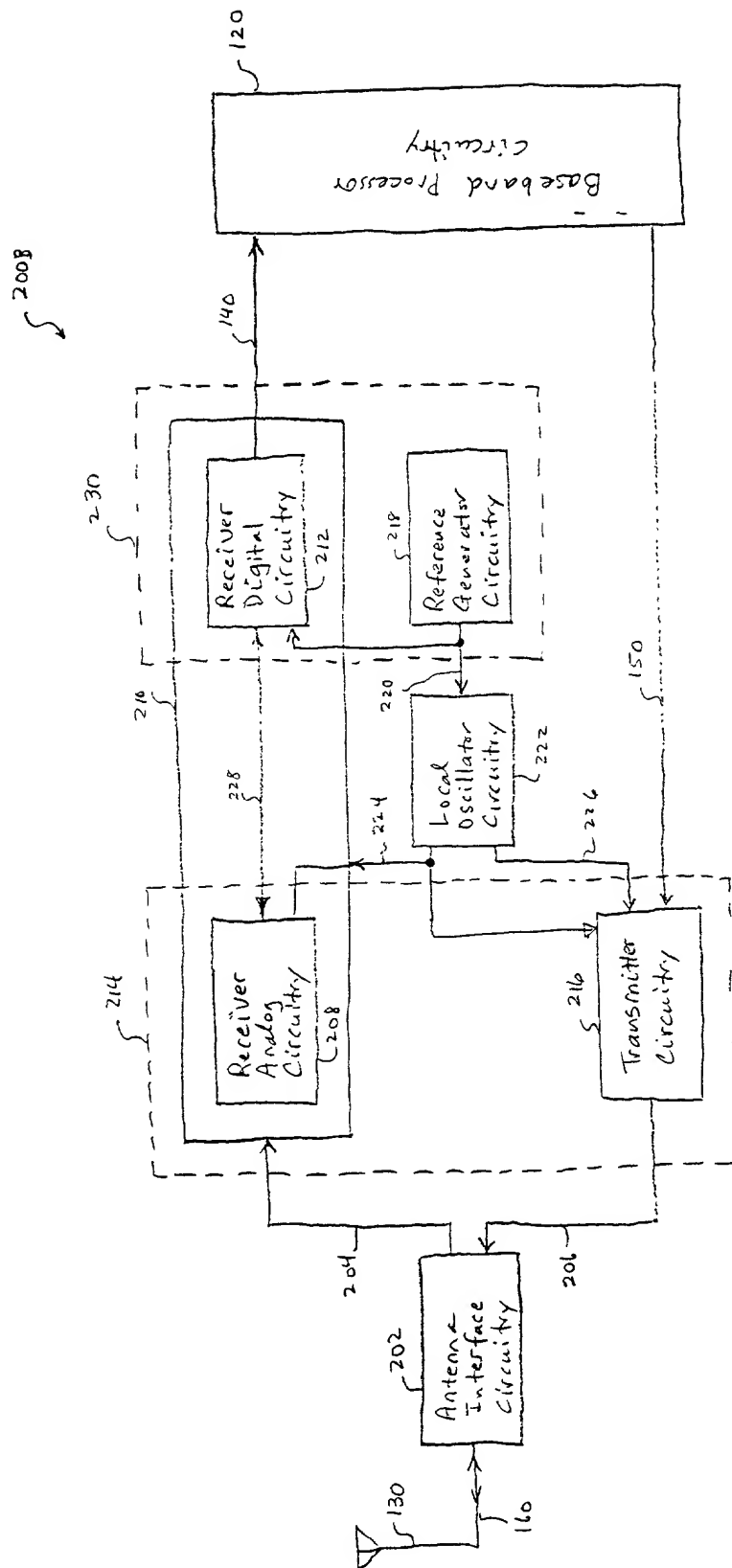


FIG. 2B

200C

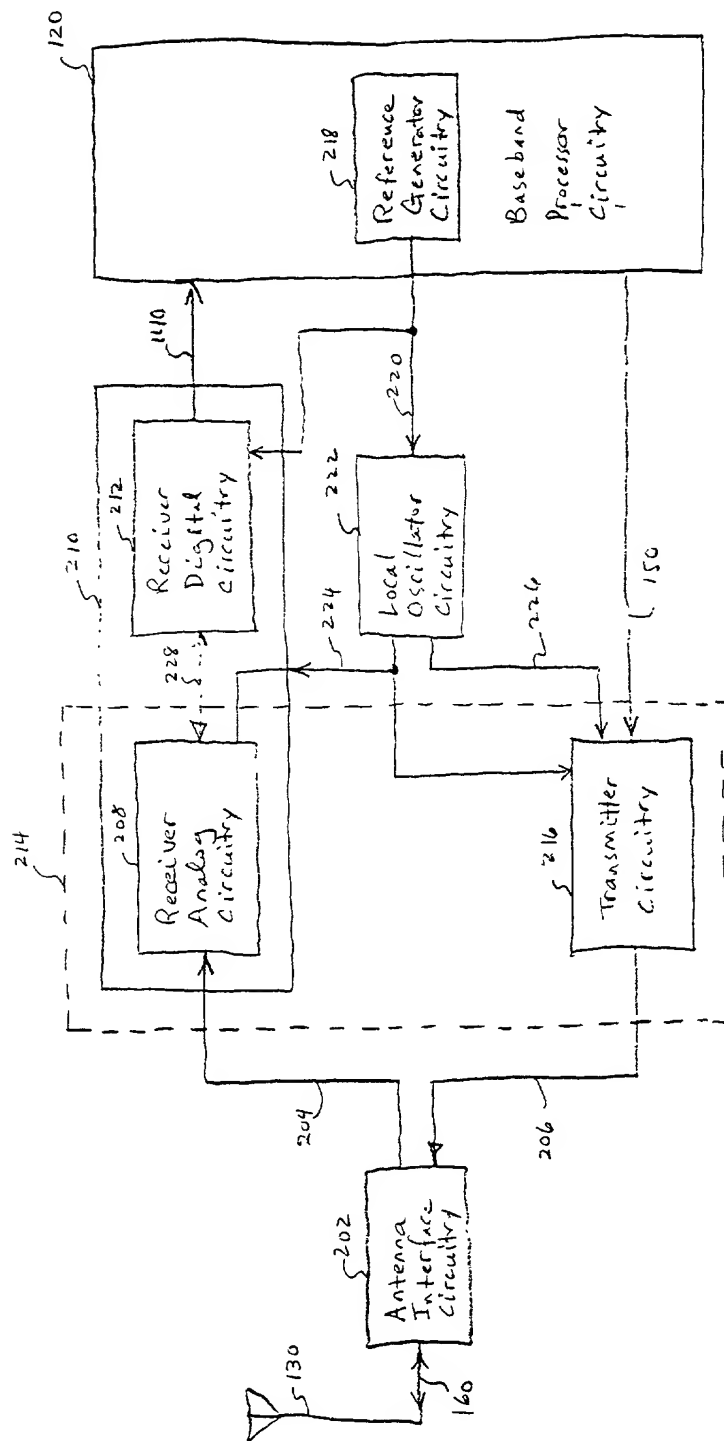


FIG. 2C

200D

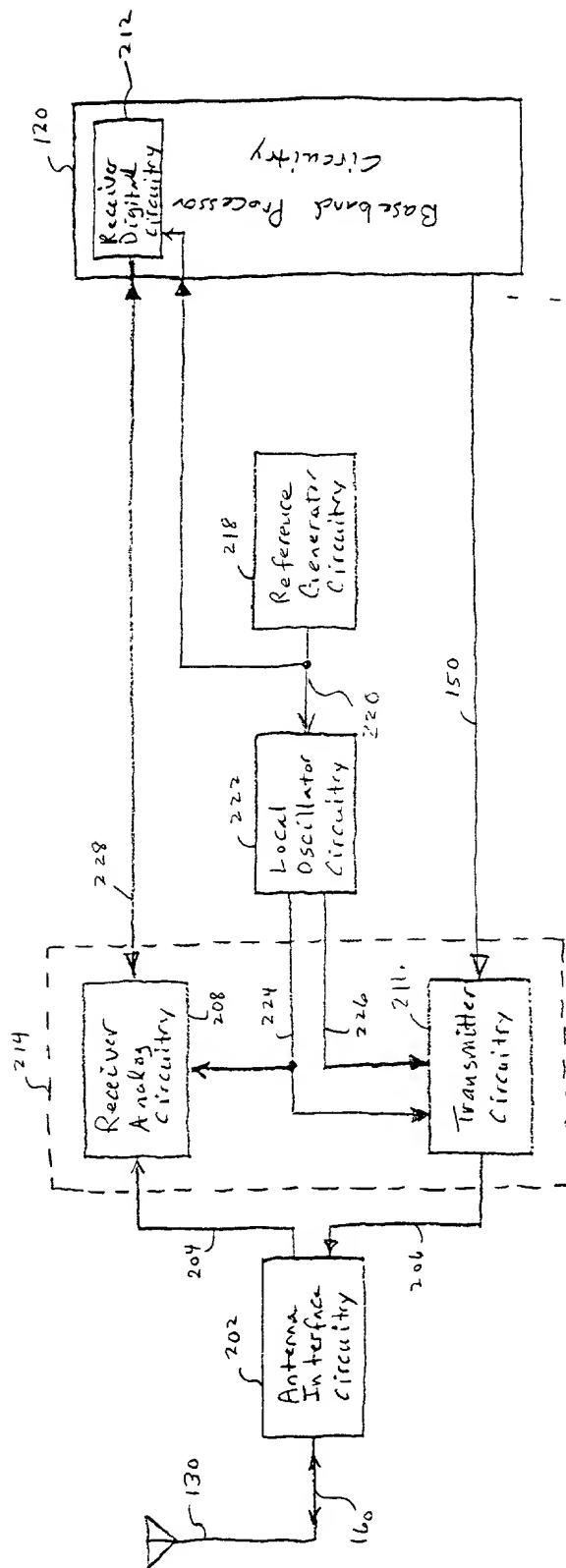


FIG. 2D

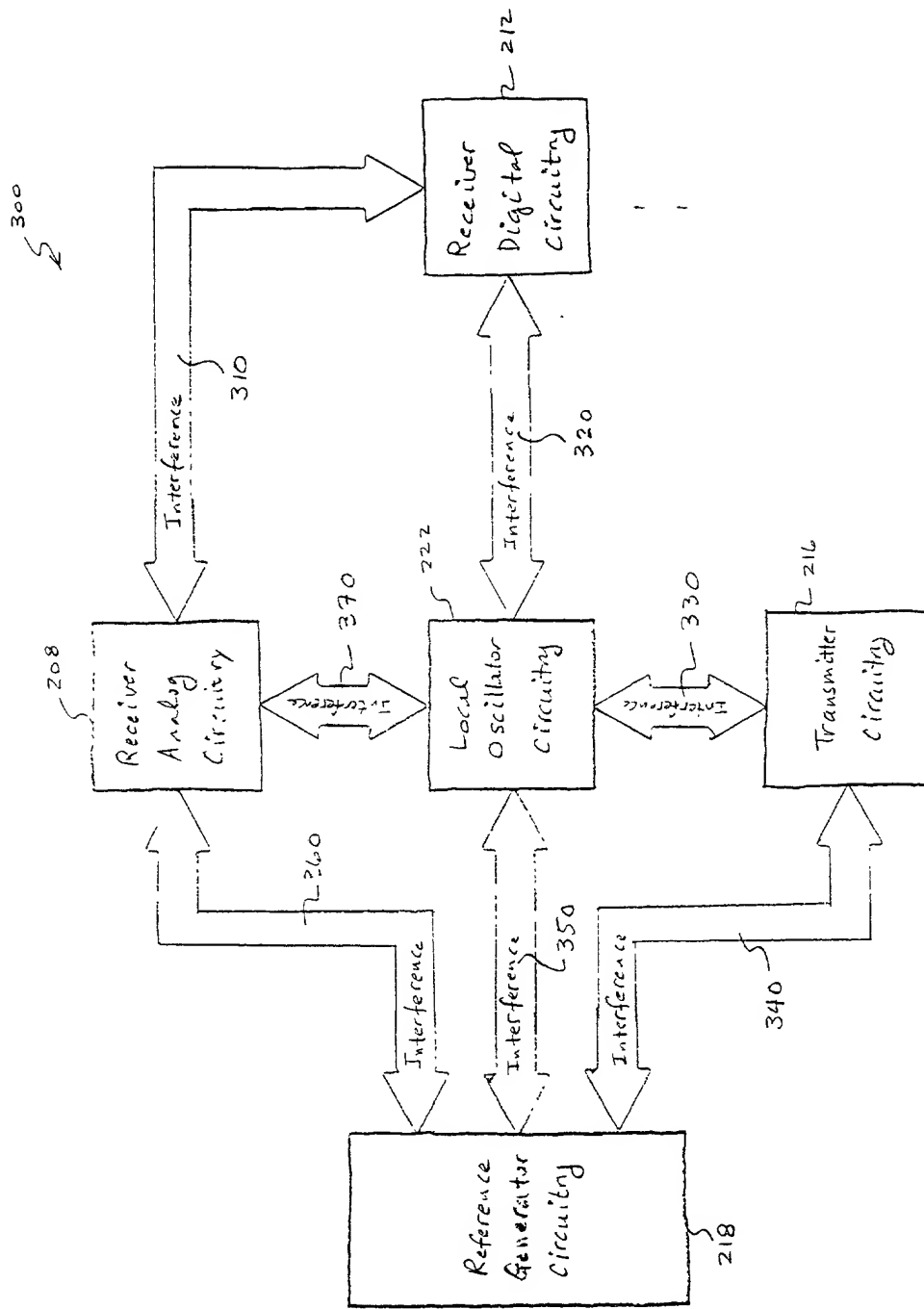


FIG. 3

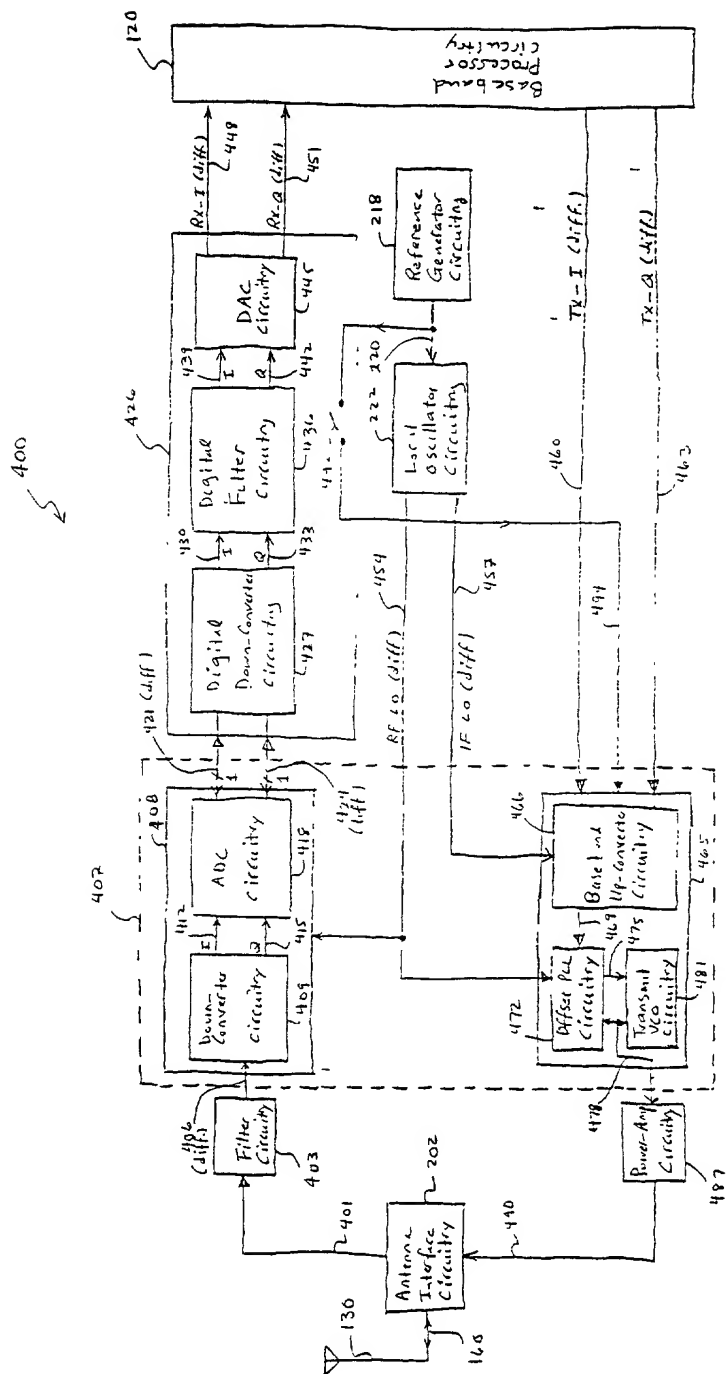


FIG. 4

500

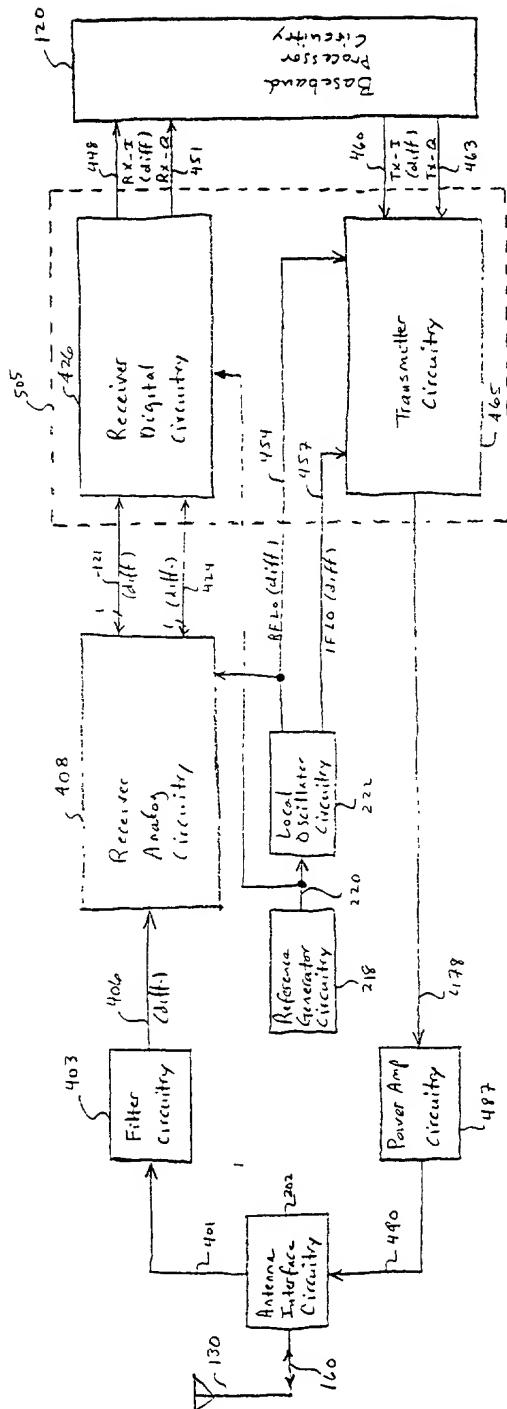


FIG. 5

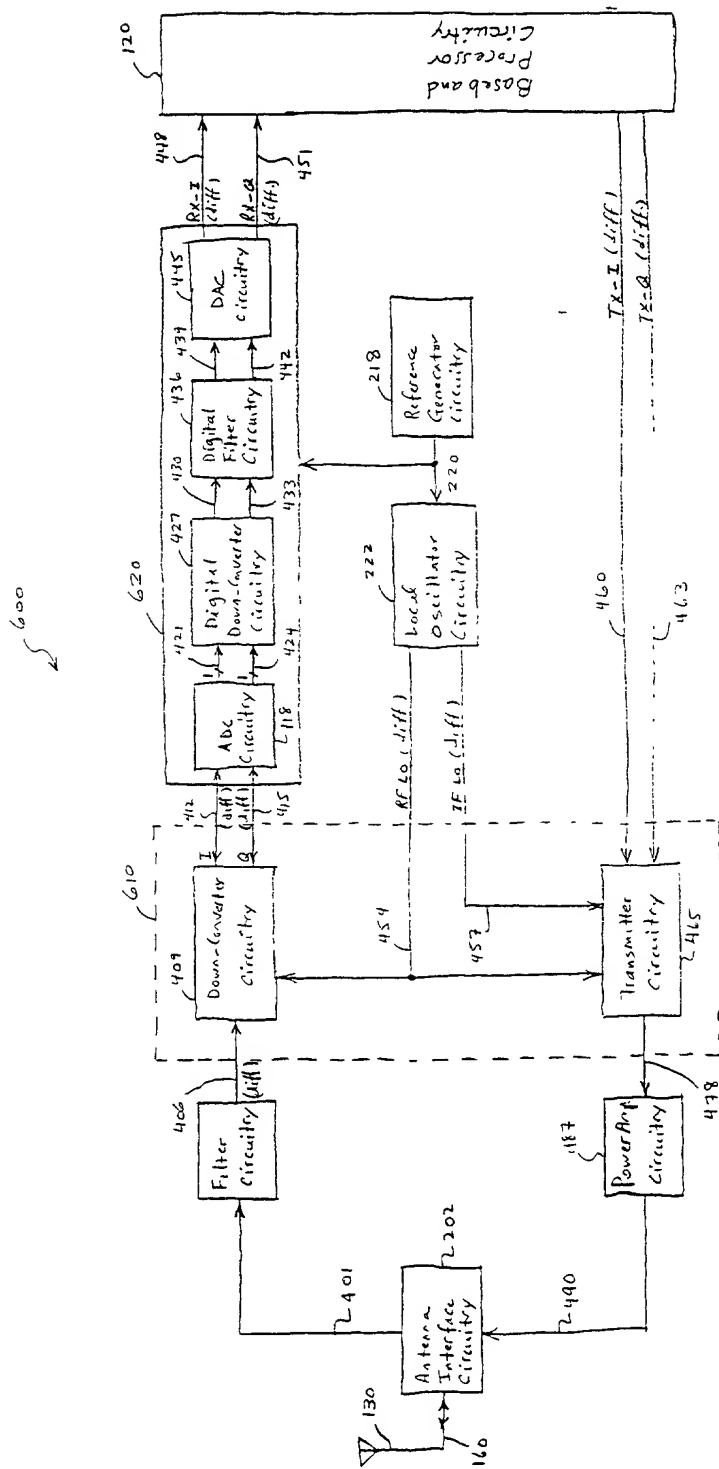


FIG. 60

700

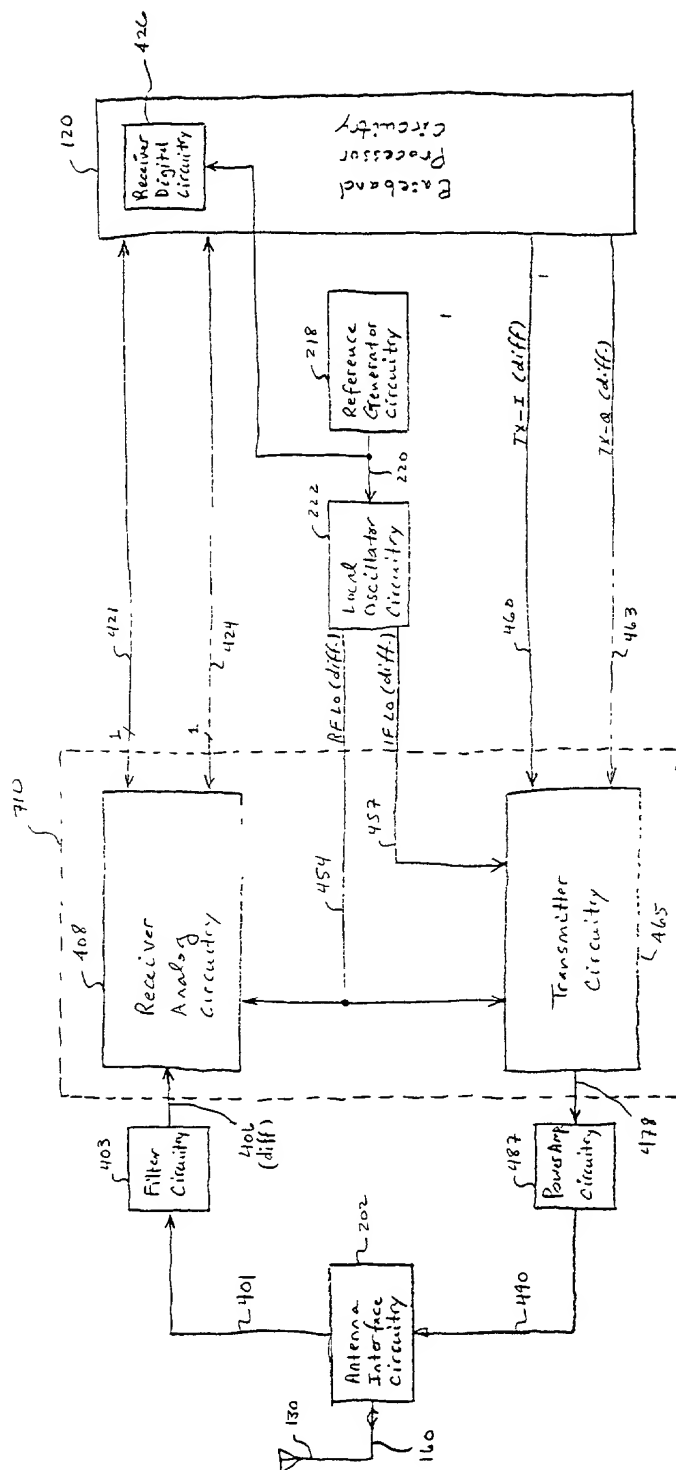


FIG. 7

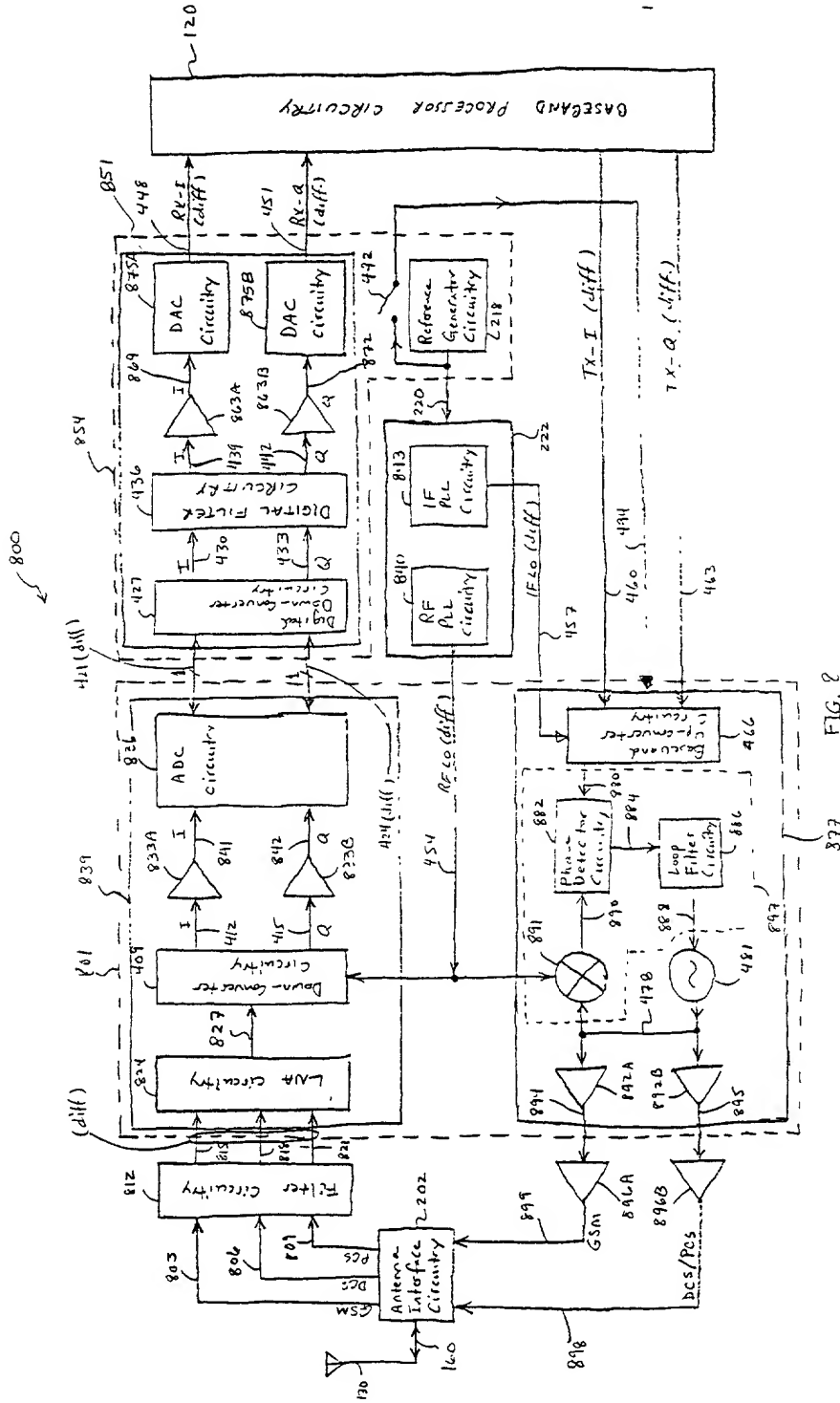


FIG. 2

FIG. 1A is a block diagram of a system 100 for receiving data from a transmitter 110. The system 100 includes a baseband receiver circuit 120, a receiver digital circuitry 130, and a receiver analog circuitry 140. The baseband receiver circuit 120 receives a signal from the transmitter 110 and outputs a signal to the receiver digital circuitry 130. The receiver digital circuitry 130 outputs a signal to the receiver analog circuitry 140. The receiver analog circuitry 140 outputs a signal to a processor 150. The system 100 also includes a control circuit 160 that controls the operation of the baseband receiver circuit 120, the receiver digital circuitry 130, and the receiver analog circuitry 140. The control circuit 160 is connected to the baseband receiver circuit 120, the receiver digital circuitry 130, and the receiver analog circuitry 140. The control circuit 160 is also connected to a power supply 170. The power supply 170 provides power to the baseband receiver circuit 120, the receiver digital circuitry 130, and the receiver analog circuitry 140. The system 100 is configured to receive data from the transmitter 110 and process the data to extract information. The system 100 is suitable for use in a variety of applications, including wireless communication systems, data storage systems, and data processing systems.

FIG. 1A

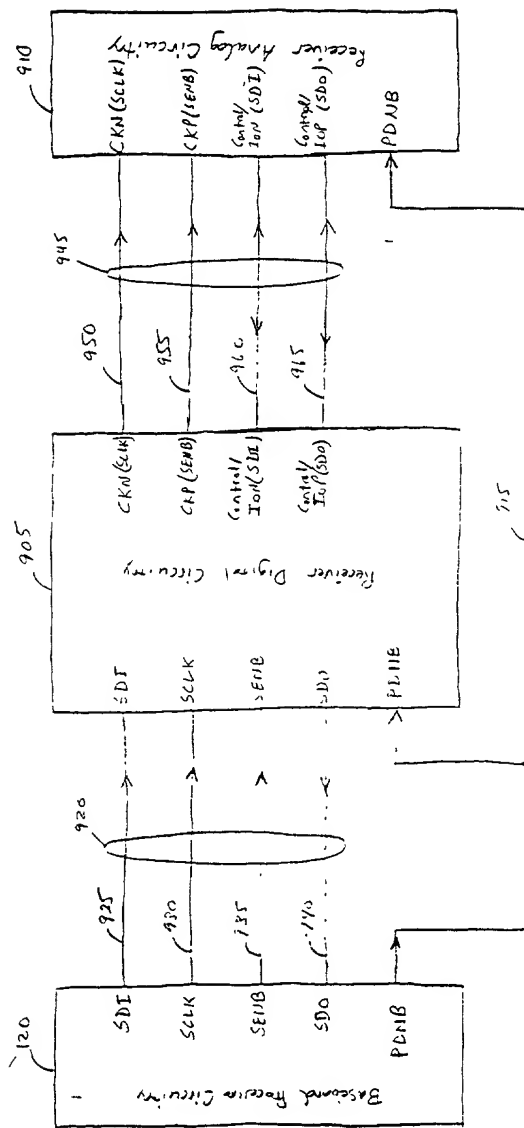


FIG. 1A

FIG. 9A

900B

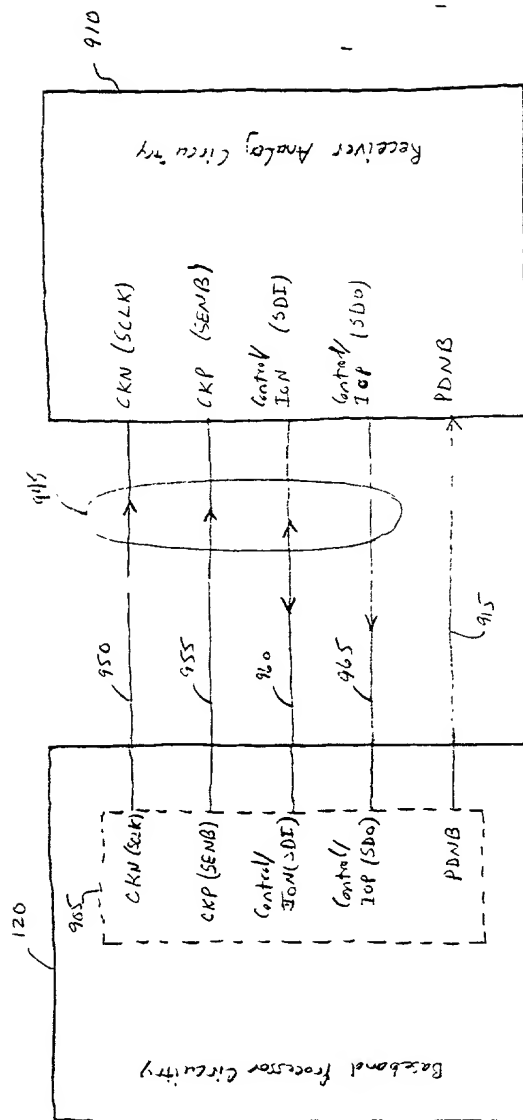


FIG. 9B

FIG. 10 is a block diagram of a system 1000 including a Baseband Receiver Circuit 120, a Receiver Digital Circuit 915, and a Receiver Analog Circuit 910.

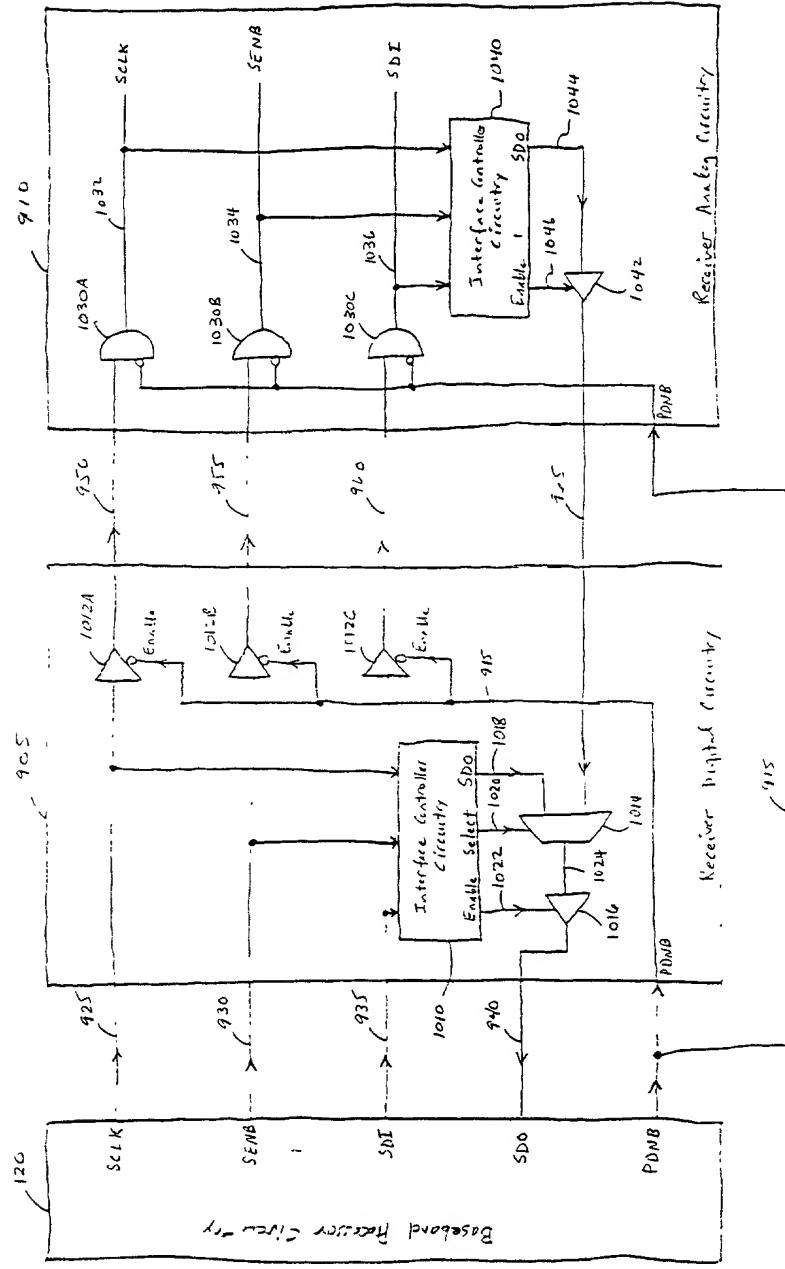
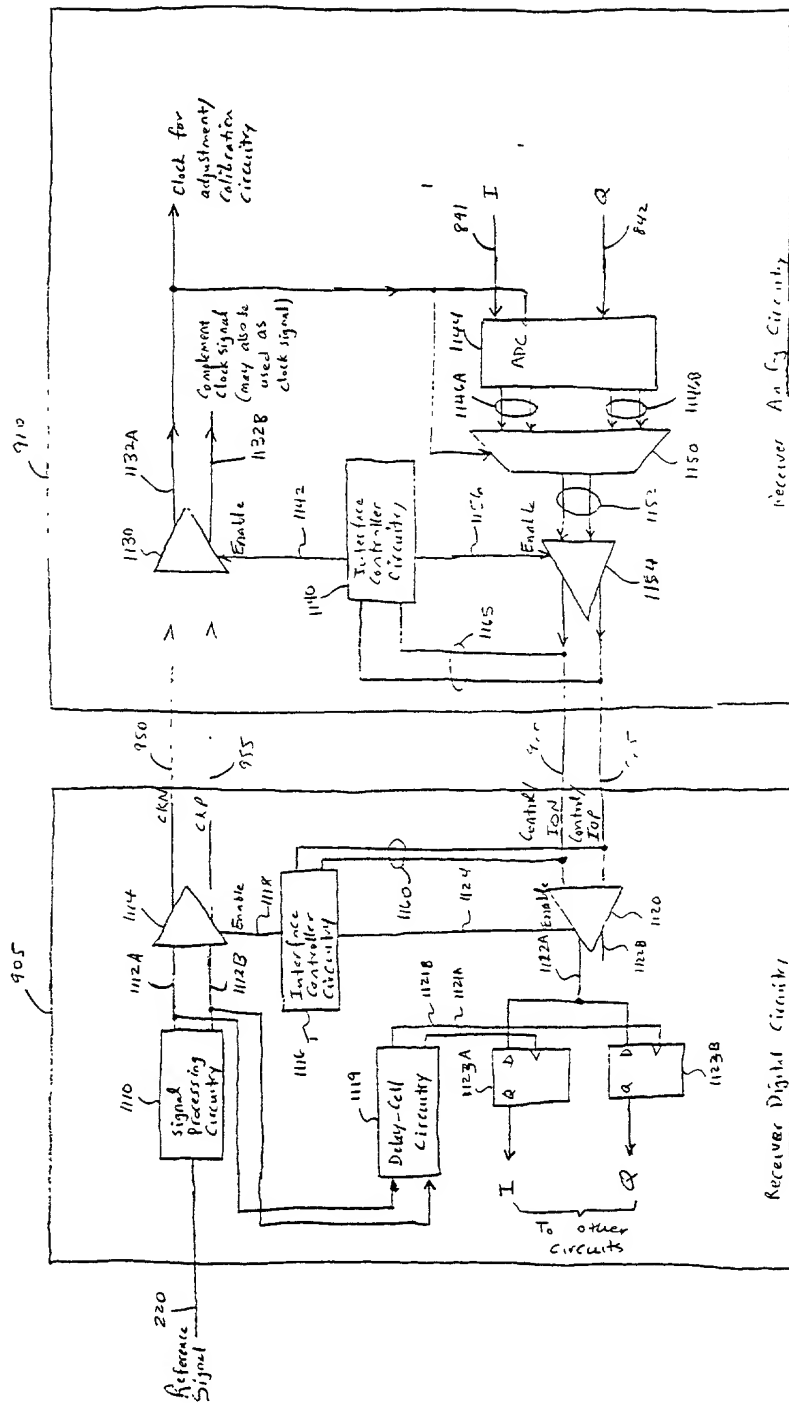


FIG. 10


$$F I \div 11A$$

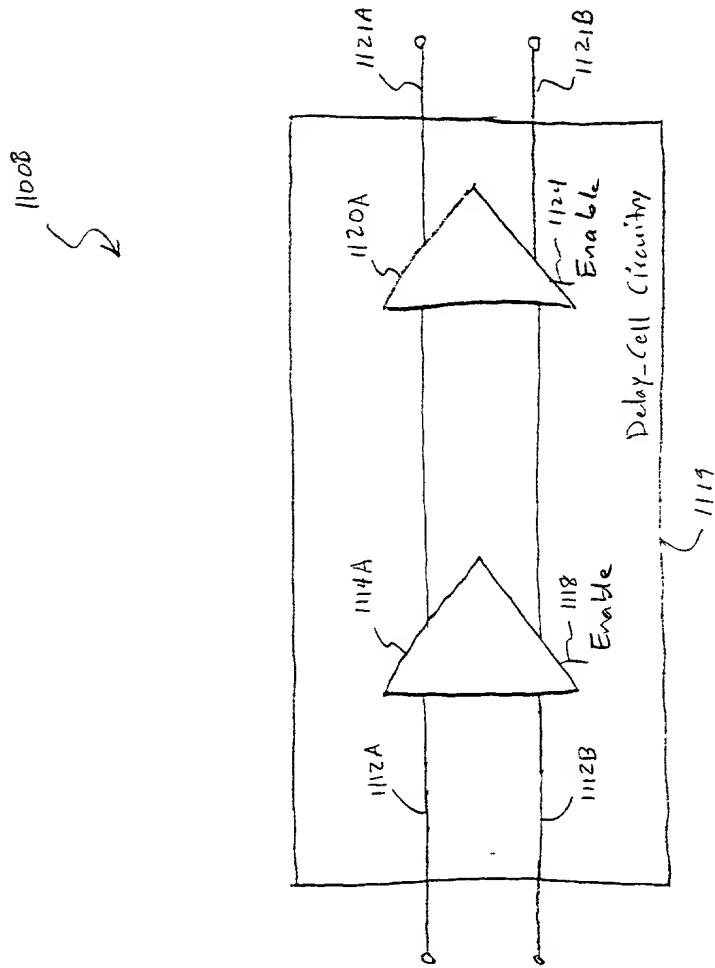


FIG. 11B

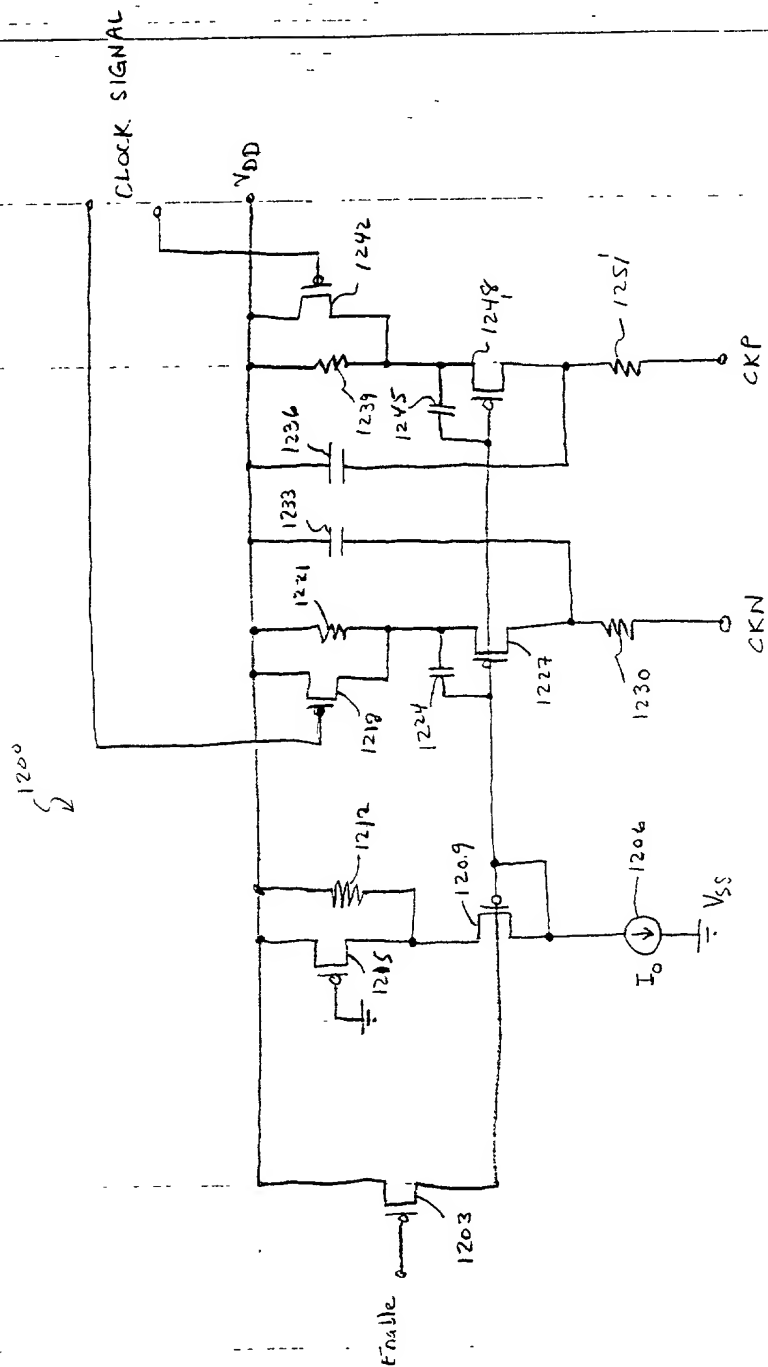
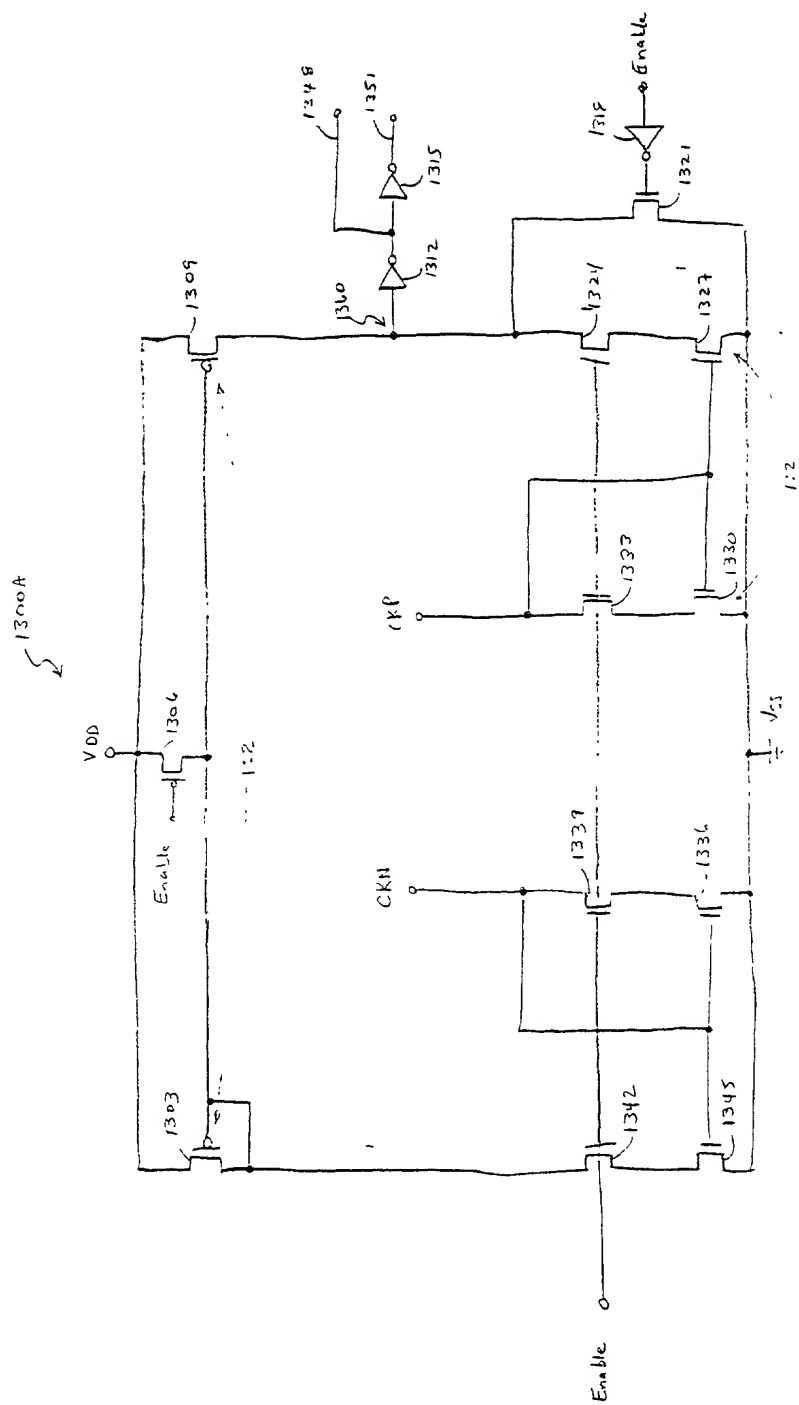


FIG. 12



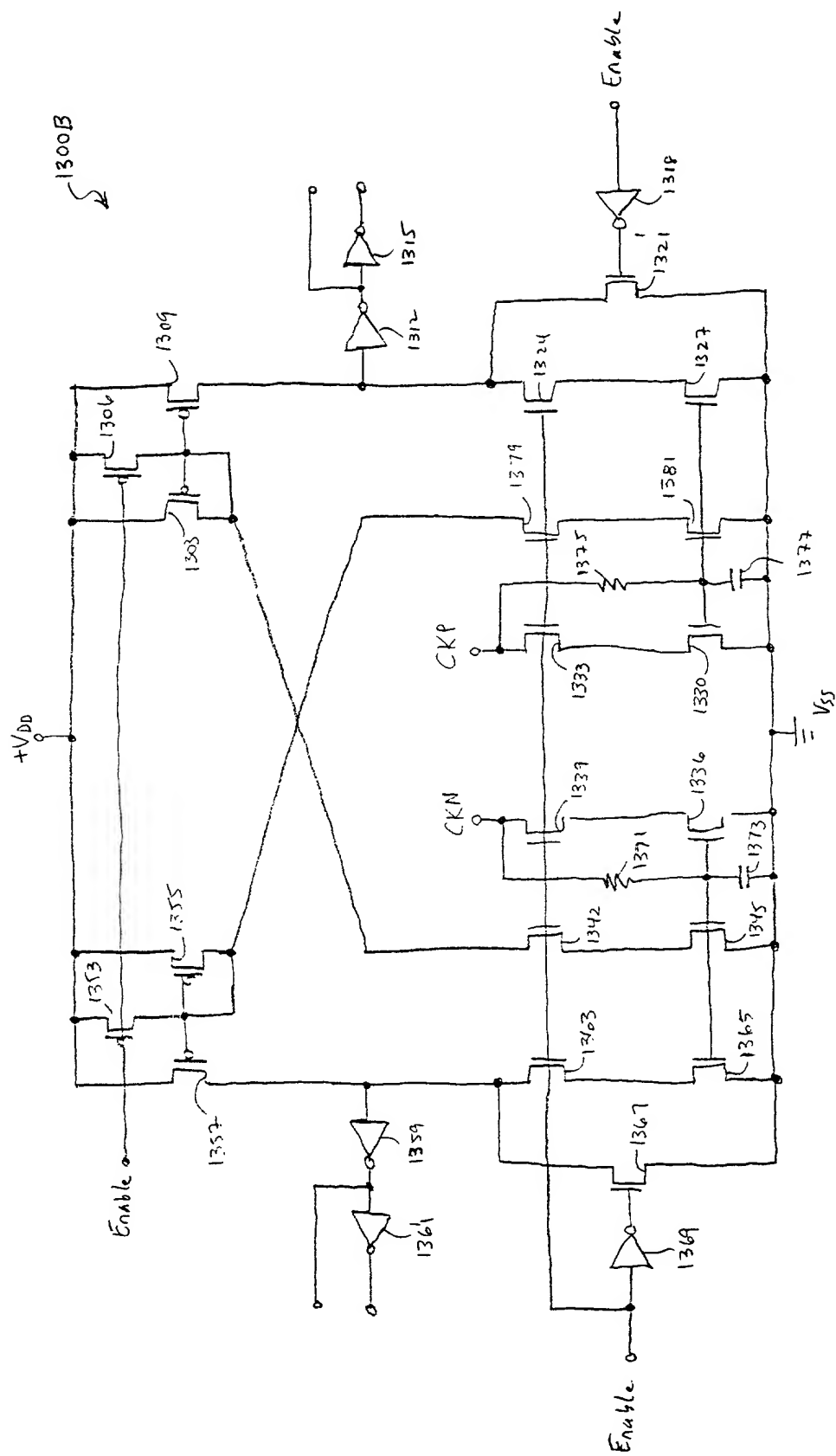


FIG. 13B

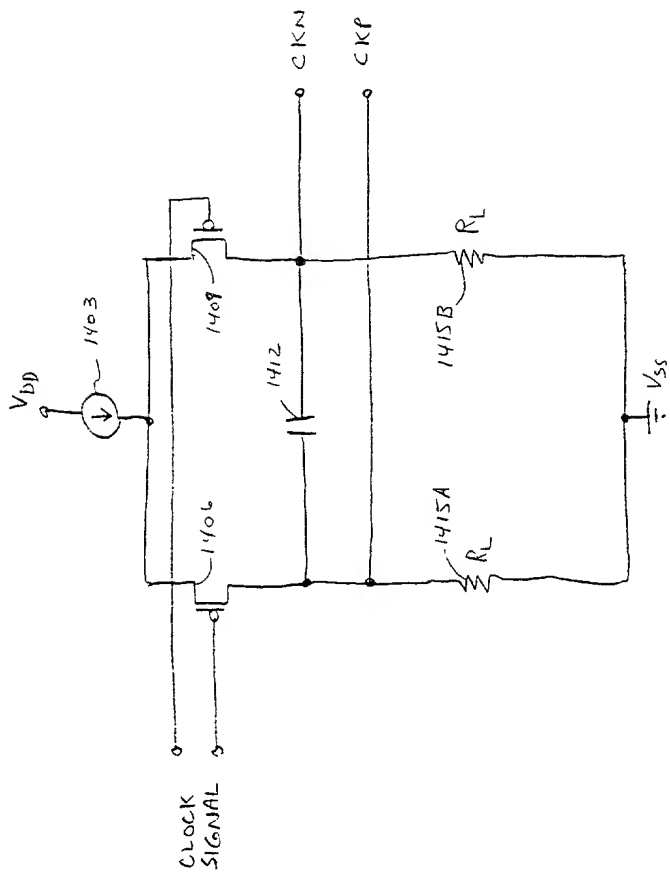


FIG. 14

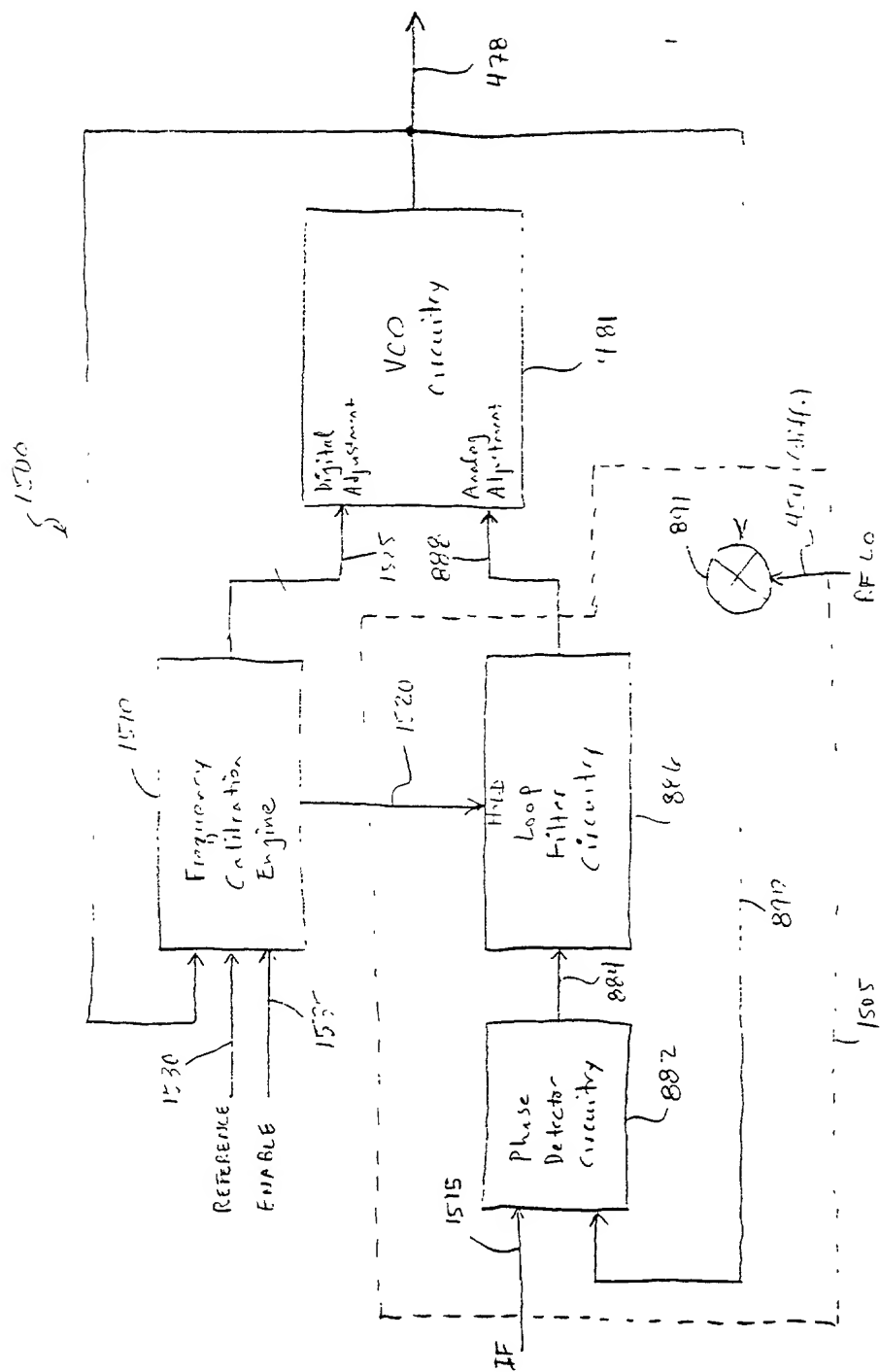


FIG. 15

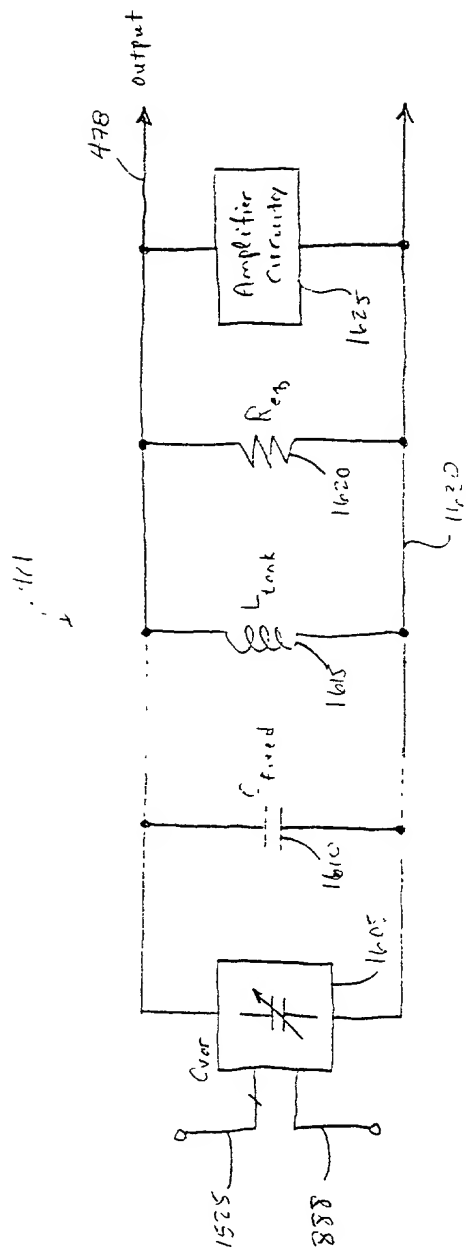


Fig. 16

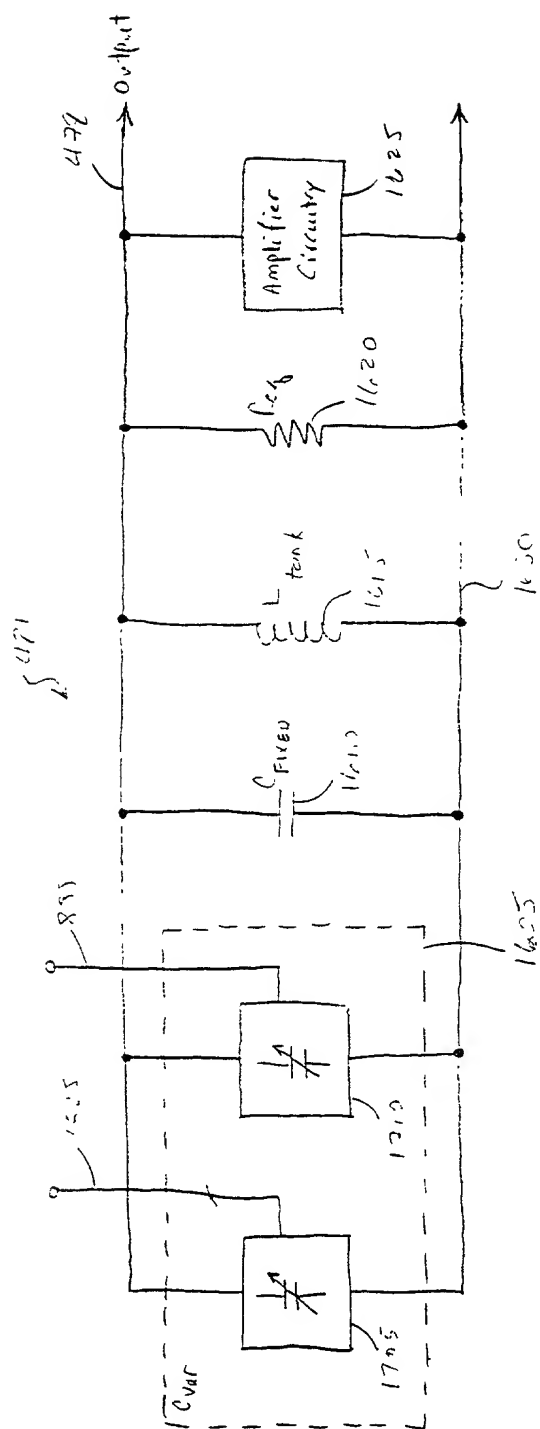
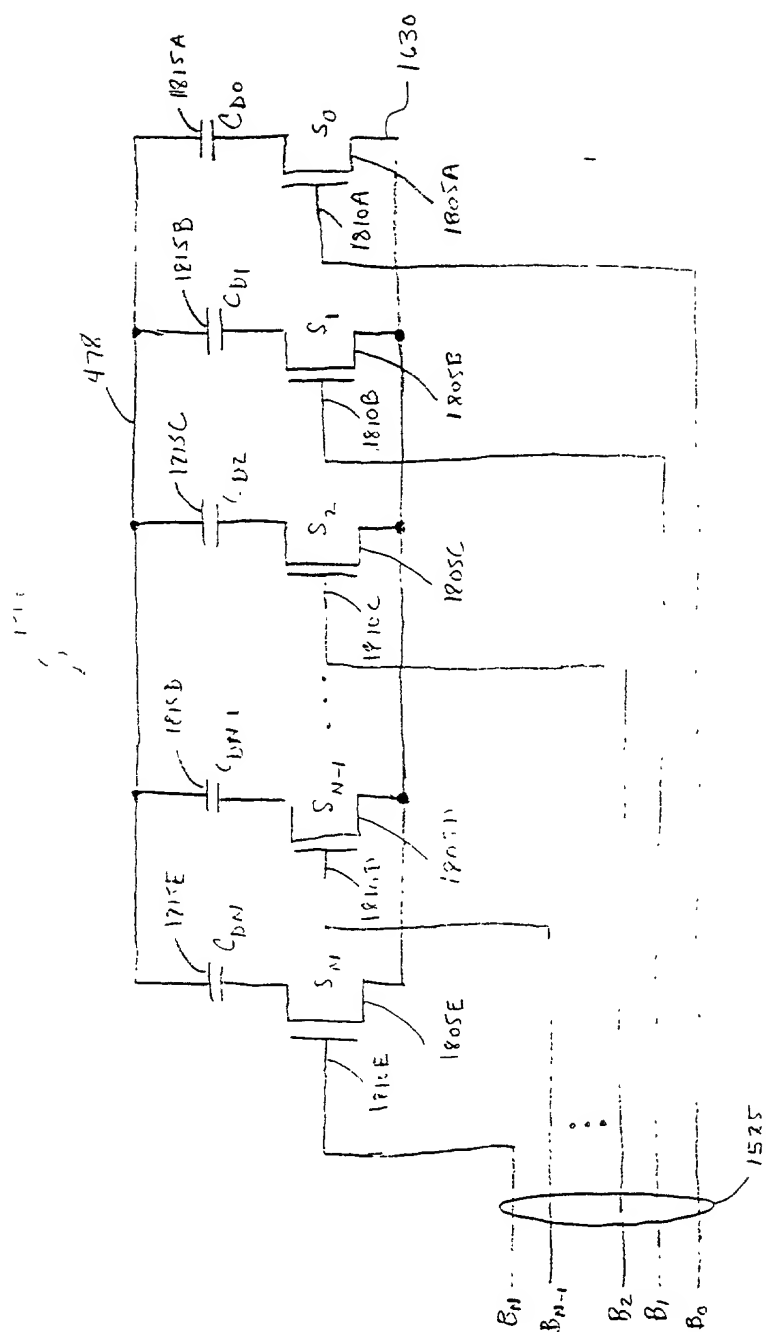


Fig. 17

[illegible]

1900A

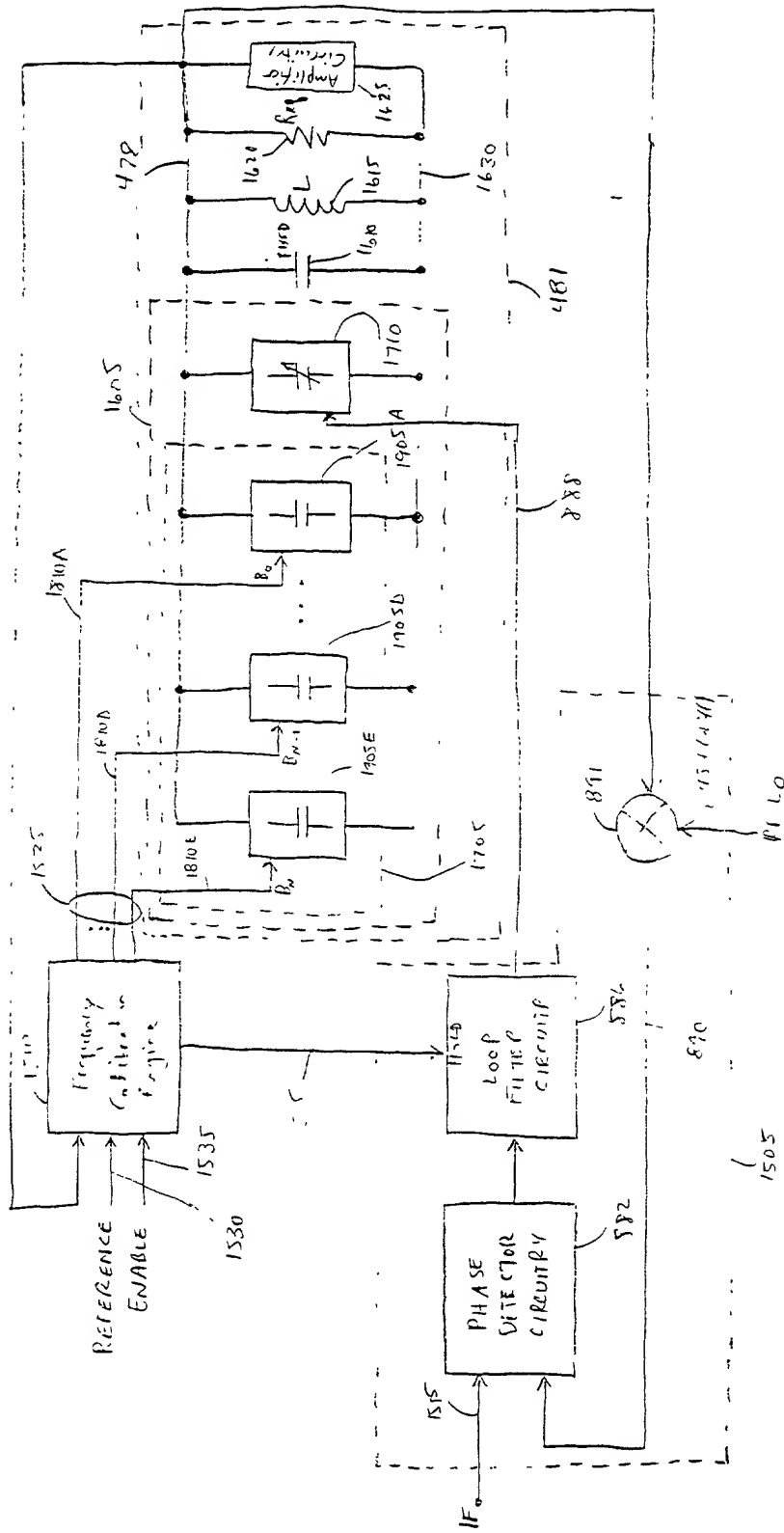


FIG. 19

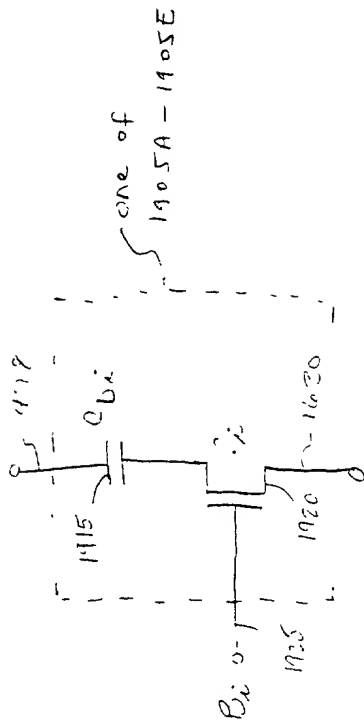


Fig. 19B

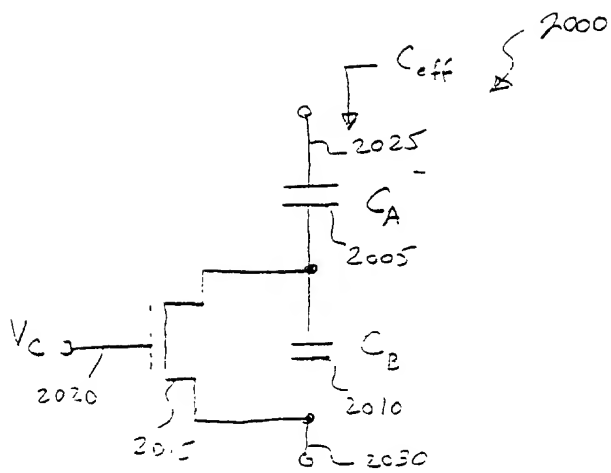


FIG. 20

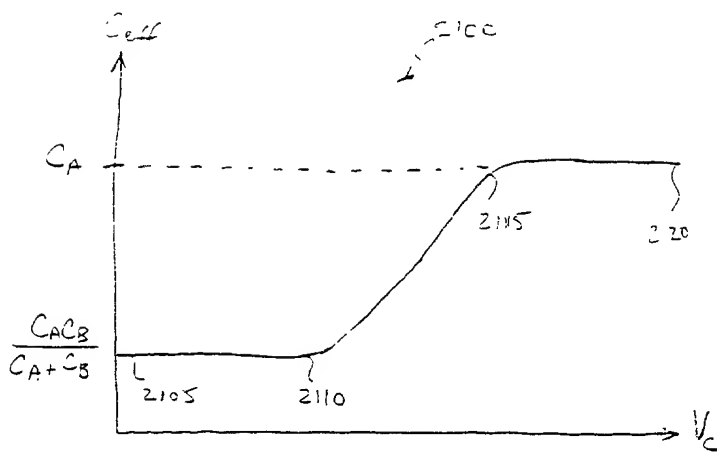


FIG. 21

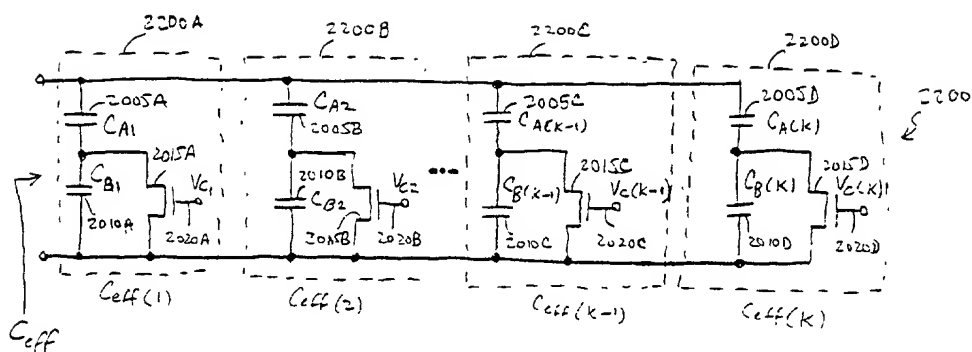


FIG. 22

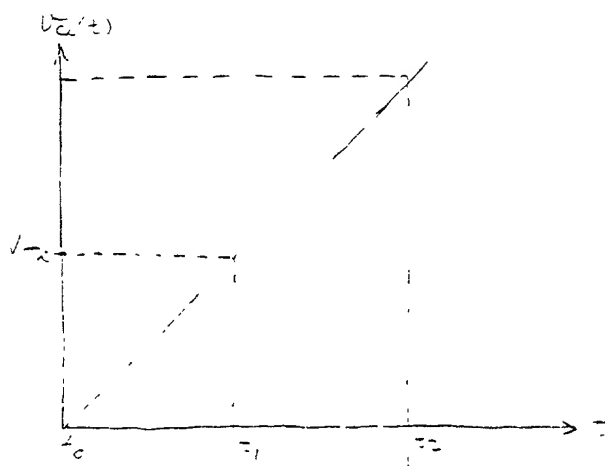


FIG. 22A

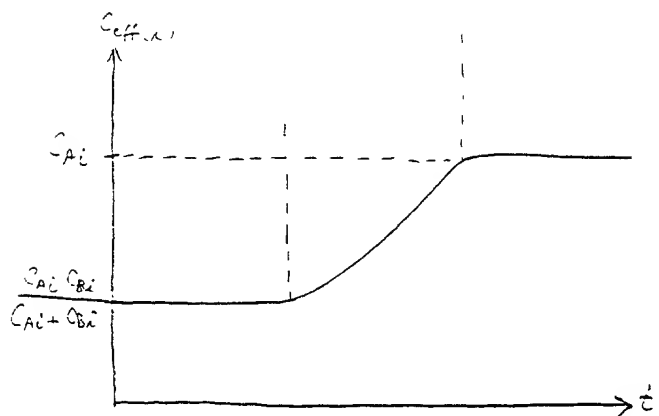


FIG. 22B

FIG. 24A

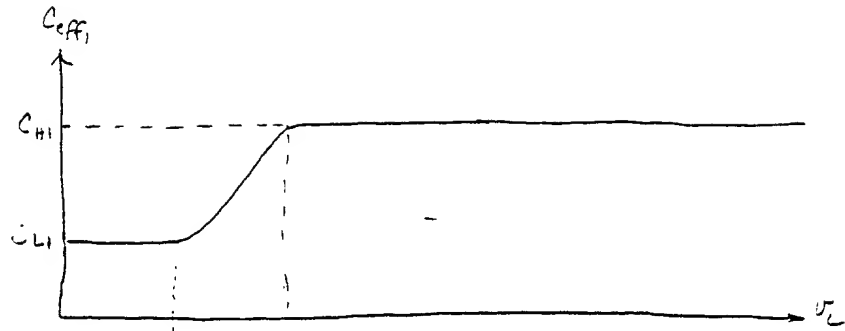


FIG. 24B

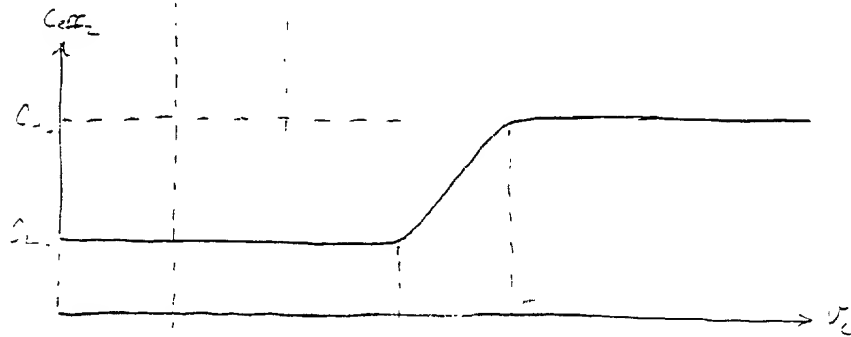


FIG. 24C

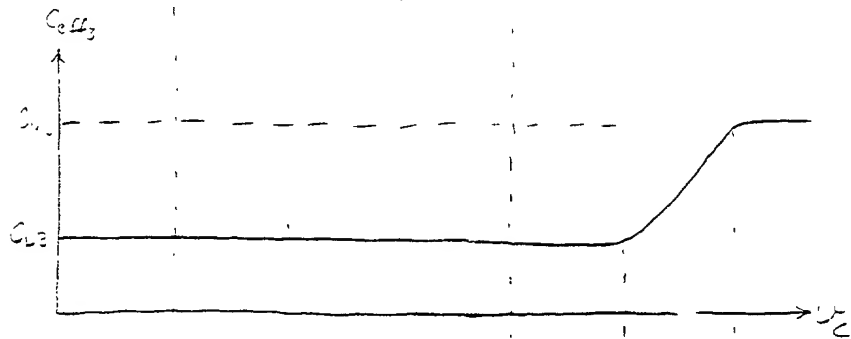
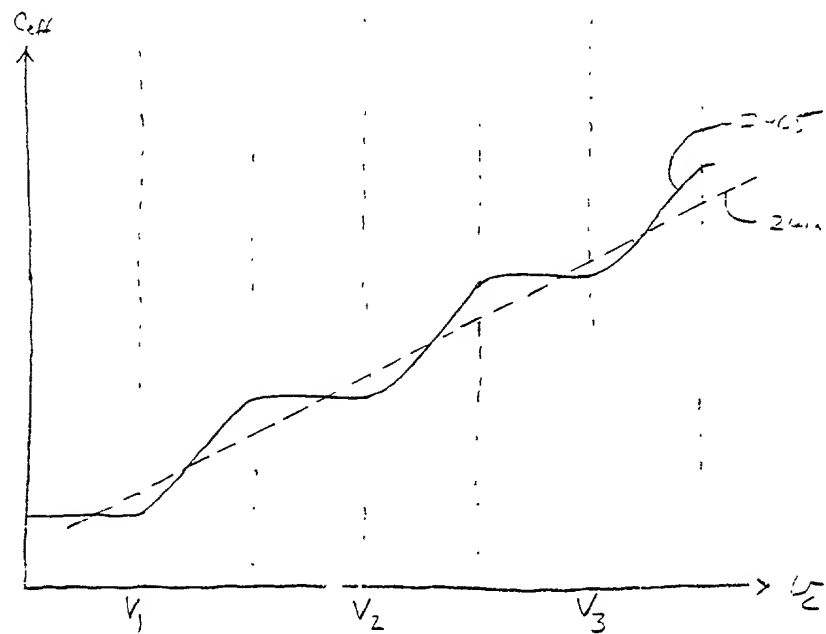
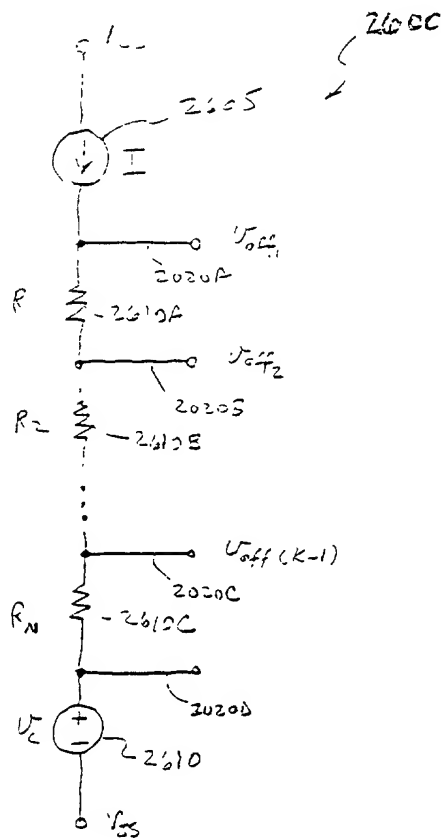
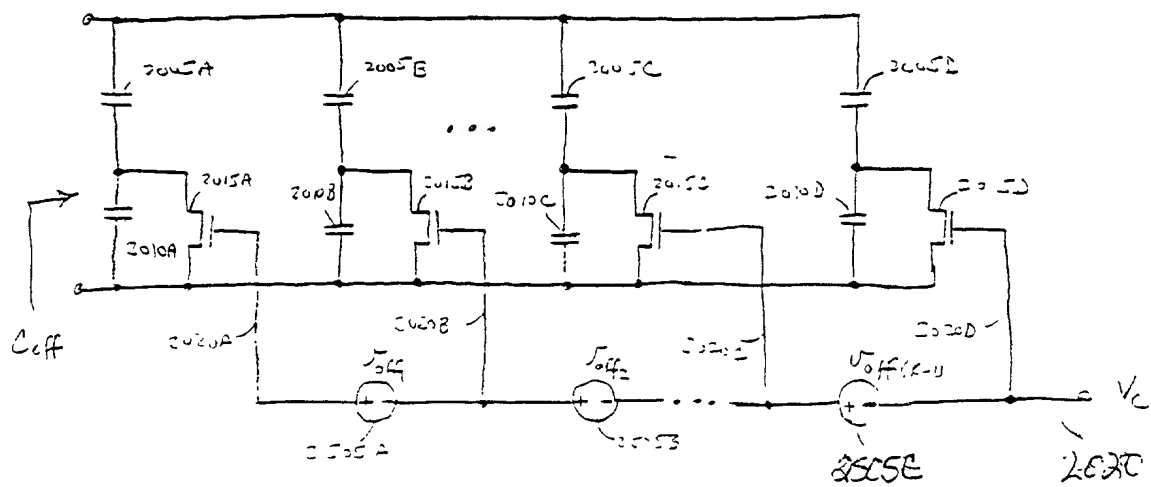
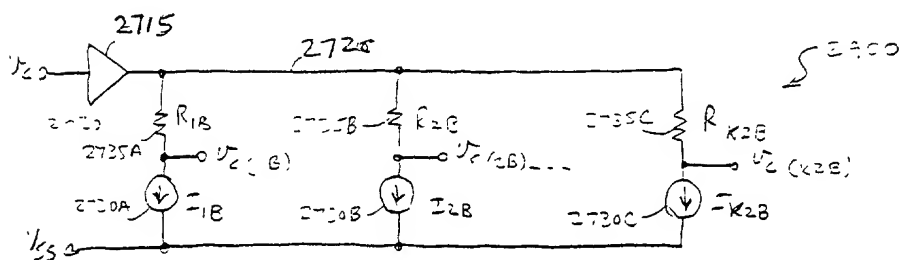
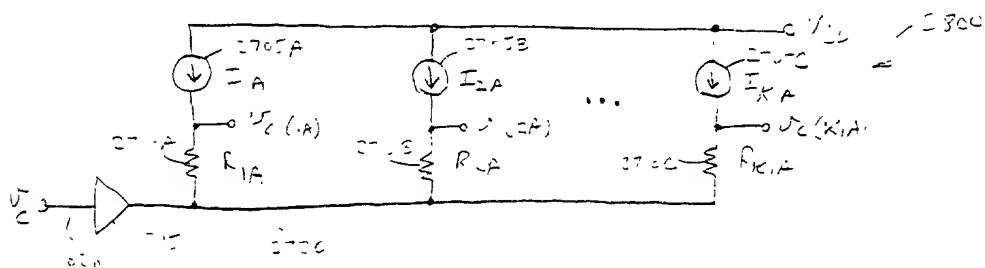
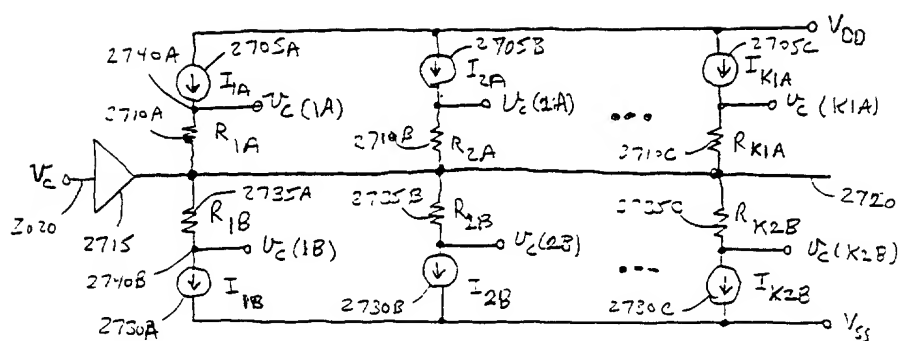


FIG. 24D







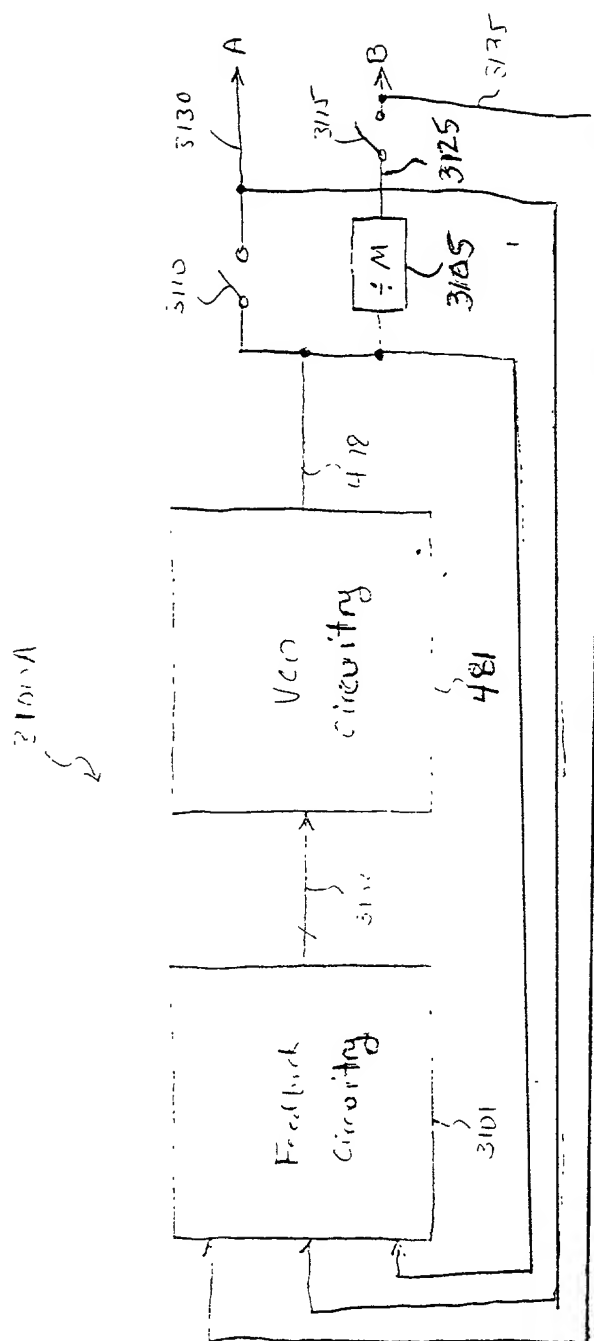
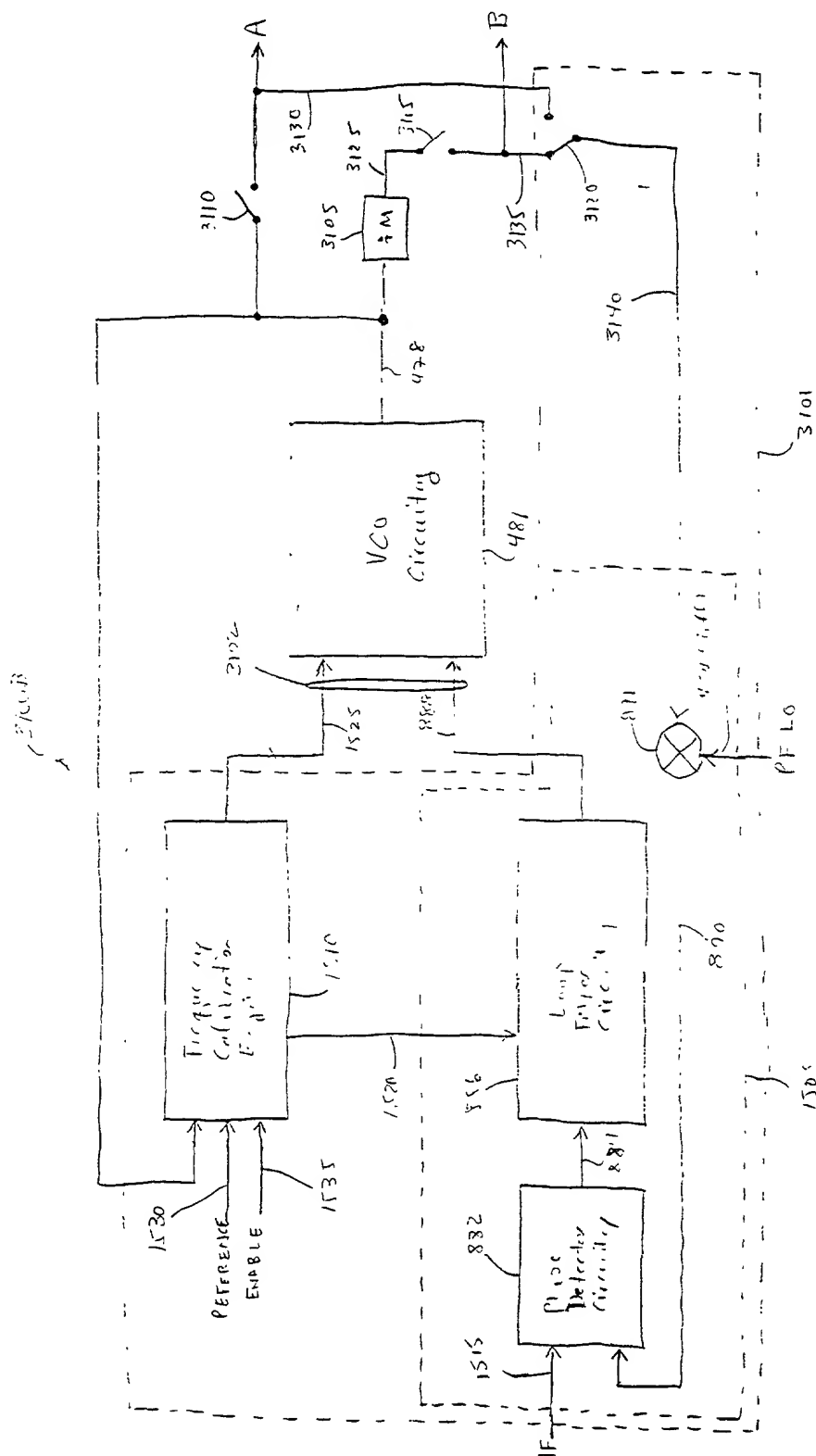
[illegible]

Fig. 1A



216.21.3

3-100

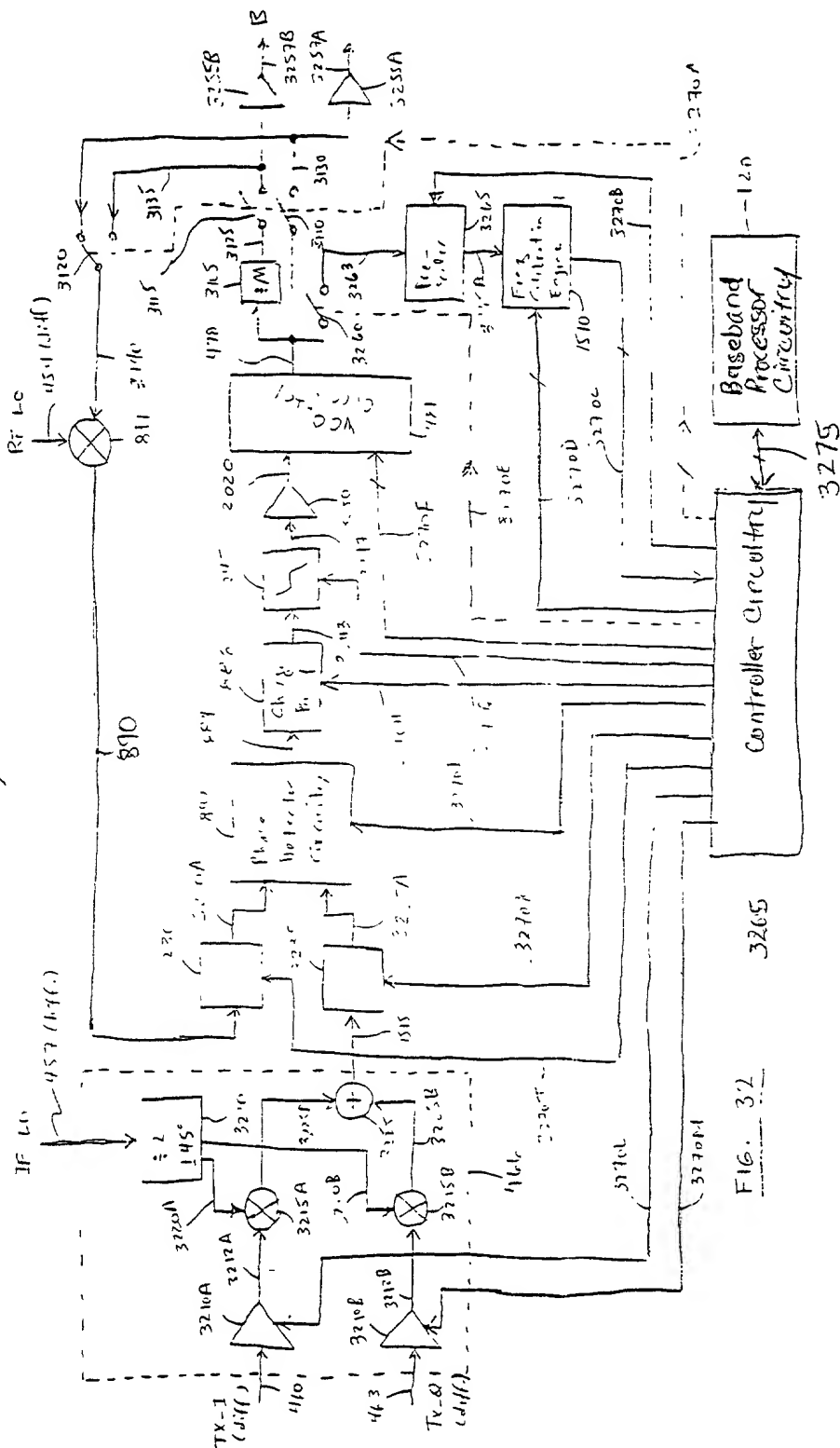


FIG. 32

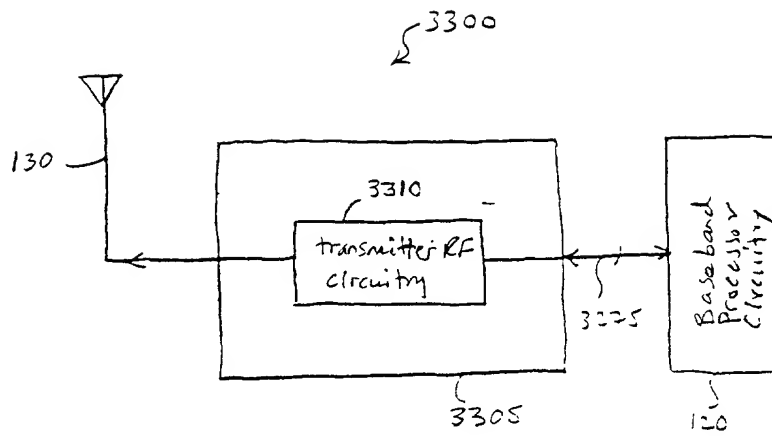


FIG. 33

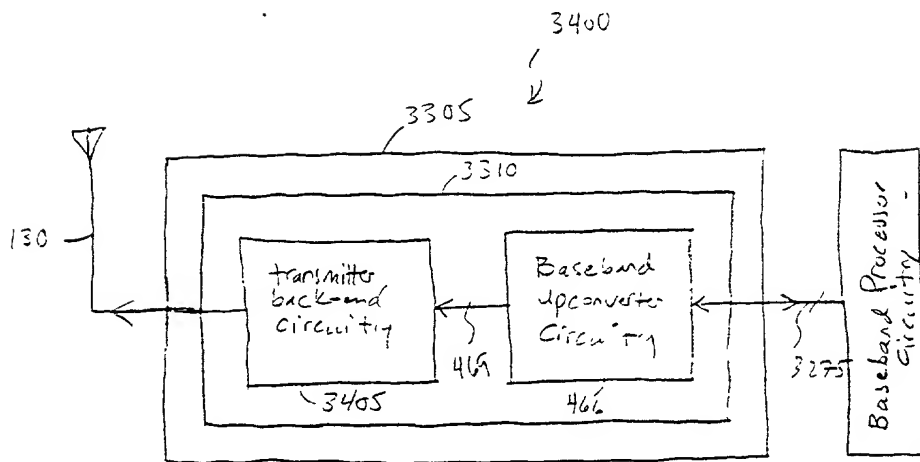


FIG. 34

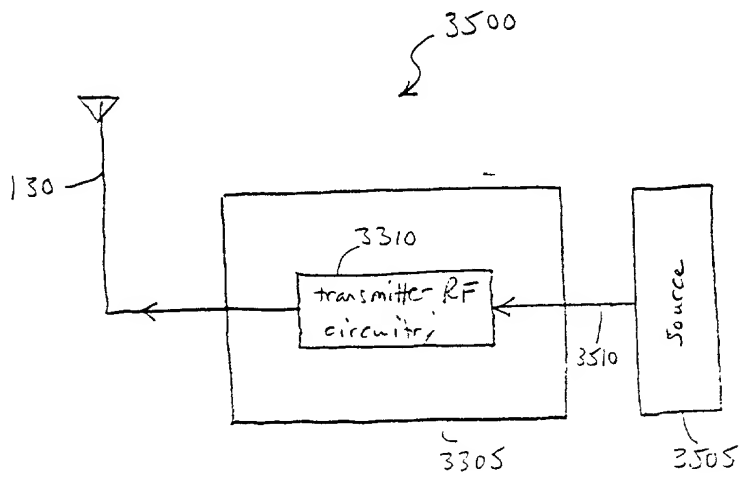


FIG. 35